

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of	)	PATENT APPLICATION
	)	
Inventor(s): Donald Paul Richmond II et al.	)	
	)	Group Art Unit: Unassigned
Application No.: New	)	
	)	Examiner: Unassigned
Filed: November 21, 2003	)	
	)	
Title: Wafer Level Burn-In and Electrical	)	
Test System and Method	)	
	)	

Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

Please amend the above-identified application as follows:

**Amendments to the Specification:**

At page 1, after the title, please insert --

RELATIONSHIP TO CO-PENDING APPLICATIONS

This application is a continuation of U.S. Application 09/865,957, filed May 25, 2001, which is a divisional of U.S. Application 09/353,121 filed July 14, 1999, now issued as U.S. patent 6,562,636. U.S. Applications 09/865,957 and 09/353,121 are incorporated herein by reference in their entirety. --

**In the Claims:**

Please cancel claims 22-26.

1. (Original) A burn-in system comprising: